

# Stratix-4 TAI Logic / Verification Module Product Table

	Quad	Dual		Single				VM	
	4S820	4S820	4S530	4S820A	4S530A	4S820B	4S530A	4S360GX	4S180GX
ASIC Logic Gates	32.8M	16.4M	10.6M	8.2M	5.3M	8.2M	5.3M	3.6M	1.8M
FPGA Memory	132Mbits	54Mbits	35Mbits	35Mbits	27Mbits	35Mbits	27Mbits	22Mbits	13Mbits
DDR3 SO-DIMM slot	2	1	1	1	1	N/A	N/A	N/A	N/A
DDR2 SO-DIMM slot	2	1	N/A	N/A	N/A	1	1	N/A	N/A
External User IO	1920	1286	1102	867	742	870	742	480	480
Inter-FPGA Nets	300	454	382	N/A	N/A	N/A	N/A	N/A	N/A
PCIe Gen2 Lanes	N/A	N/A	N/A	N/A	N/A	N/A	N/A	x4	x4
SMA Gigabit Transceivers	N/A	N/A	N/A	N/A	N/A	N/A	N/A	2 pairs	2 pairs
User Clocks	19	20	20	20	20	20	20	6	6
On-Board Programmable Clocks	6	3	3	3	3	3	3	N/A	N/A